AMENDMENTS

In the Specification

Please amend the following paragraphs as indicated:

For the paragraph beginning at page 6, line 10:

Fig.1 shows three versions of Dynamic Lot Dispatching corresponding to three generations of development. Two prior art systems with the column headings I, Quick Scheduler, 12 and II, DLD 14, and the On-line DLD 16 system of the invention, with the column heading This new version - On-line DLD. Various characteristics of the three systems are indicated in the first column. As can be seen in the first row corresponding to System Type, I is off-line , II is Online, Real [[Tine]] Time and, the system of the invention is on-line and works in real time. The Algorithm type row shows that I uses a Heuristic Algorithm, where the later versions use Multi-Priority Factor, as well as Multi-Dispatching Algorithm. As noted in the rows labeled Take Care MPS and Take Care Lot Due Date, the system of the invention takes into account the Master Production Schedule and the lot due date.

For the paragraph beginning at page 8, line 5:

The cause and effect diagram of Fig. 2 gives an overall view of what affects Dispatch in a Test Foundry 210. Production Performance Indices 202 (CLIP percentage, Change over time, Tester Unitization, Planer MO, and Manual dispatch effort), Special Dispatch Properties 204 (Exception Management Urgent lot Interruption behavior, Re-entrant work flow, Tester Capability, and Sequence Dependent Setup time), Auxiliary Apparatus or Tester Constraints 206 (Probe Card, Load Broad, Socket, Tester, Handler, and Prober), and Production Mode 208 (Production Moe PKG test, Engineering Mode PKG test, Production Mode wafer sort, and

Engineering Mode wafer sort) all impact how dispatch will occur. <u>Production</u> <s>Performance</s> Indices 202 include such things as CLIP% (amount of reached Master Production Schedule (MPS) items/amount of total MPS items), tester utilization (total running time/total capacity time, setup time for changeovers, tardiness of lots, manual dispatching effort, and planners' mistake operations (Planner MO). Special Dispatch Properties 204 include such things as sequence dependent setup time, re-entrant work flow coming from different routing or re-sort workflow needing different priority assignment, exception management by production control and engineers, tester capability, and urgent lots interruption behavior. Auxiliary apparatus or tester constraints 206 include load boards, testers, probers, probe cards, sockets, and handlers. Production Mode 208 includes production mode wafer sort and package test and engineering mode wafer sort and package test.

For the paragraph beginning at page 10, line 13:

Additional logic shown in Fig. 4C shows a test for looking for an Idle Tools that exist & P/C(L/B) available 441. A Yes, Interrupts the Tool 402 as does the other decisions results shown. If negative, then check are made for Tools with the same product 440, and Find the tool with the lowest priority lot and lower than specified lot's 444. If not the same product, the test Tools with same P/C 442 is made. The tests for P/C and L/B available or needless 446 448 are made. If they are negative, Show no P/C or L/B warning message on PUI 450 454 is created, and a No P/C or No L/B warning is created for manual action. Fig. 4B shows the logic from a negative response from Find the tool with the lowest priority lot and lower than specified lot's 444. A test is made for Tools with the same product 460. If yes, then [[[Fnd]]] <u>Find</u> the tool with the lowest

priority lot of same product [[ans]] and assign this lot to this tester. 462. If not, then Find the tool

with the lowest priority lot and assign this lot to this tester 464.

For the paragraph beginning at page 12, line 14:

The second set of tests [[os]] is shown in Fig. 6B. It is Engineering Lot Dispatching 604

in which Have any engineering lot capacity on this tester 610 is a check to determine if the

amount of fixed testing time per week determined by testing site personnel for engineering lots to

be tested on any particular machine has been exceeded. Checks for Highest priority lot of same

product in Eng. Lot rank exist 634, Highest priority that use sam P/C exist 636, Highest priority

lot that use same L/B 612, Highest priority lot of same production type (CP/FT) in Eng. LRL

exist 640, and is it the next on top of Eng. LRL exist 642   The engineering lots are dispatched

automatically with manual effort only needed for setting exception rules and taking care of

special cases , thereby eliminating the need for a manual dispatching sheet. The successful lot is

passed to Pass PROMIS Constraint, Best of Lot 616.